

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,231,484 B2
APPLICATION NO. : 10/529621
DATED : June 12, 2007
INVENTOR(S) : Berenyi et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 4, Line 26, delete "incoming-data/information" and insert -- incoming data/information --, therefor.

In Column 4, Line 30, delete "simultaneously" and insert -- simultaneously --, therefor.

In Column 5, Line 44, delete "first." and insert -- first --, therefor.

In Column 5, Line 61, delete "thereof" and insert -- thereof) --, therefor.

In Column 7, Line 23, delete "a-second" and insert -- a second --, therefor.

In Column 10, Line 29, delete "A)" and insert -- 0) --, therefor.

In Column 10, Line 30, delete "channel-circuits" and insert -- channel circuits --, therefor.

In Column 18, Line 19, delete "pre-fetch/Automatic" and insert -- pre-fetch/automatic --, therefor.

Signed and Sealed this

Thirteenth Day of May, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office